IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.   :   10/518,974
Confirmation No. :   2918
Applicant   :   Richard Michael Taylor
Title (as amended):   Microprocessor Instruction Execution Method for Exploiting
                            Parallelism by Time Ordering Operations in a Single Thread at
                            Compile Time"
Filed   :   December 21, 2004
Art Unit   :   2183
Examiner   :   Huisman, David J.

Attorney Docket No. 5035-202US
                  S&L File No. P29,653 USA

FILED ELECTRONICALLY ON March 16, 2007

MAIL STOP AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY AND AMENDMENT UNDER 37 C.F.R. §1.111
TO EXAMINER'S ACTION MAILED SEPTEMBER 18, 2006

Sir:

This is in response to the Office Action mailed September 18, 2006, having a period for

response set to expire on December 18, 2006. A Petition extending the period for response for three

months, to March 19, 2007 (March 18 being a Sunday), is included herein, along with authorization

to charge the extension fee to a credit card. The following amendments and remarks are respectfully

submitted.

An amendment to the Title appears on page 2 of this paper; amendments to the claims begin

on page 3 of this paper; Remarks begin on page 8 of this paper.